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PATENT  
(5681-67300/P9675)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/696,891  
Filed: October 30, 2003  
Inventor(s):  
Robert E. Cypher

§      Examiner:      Unknown  
§      Group/Art Unit: 2131  
§      Atty. Dkt. No: 5681-67300

# **ECC FOR COMPONENT FAILURES USING GALOIS FIELDS**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

**Name of Registered Representative**

   
Signature

6/11/04  
Date

## **INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of  the references listed on the attached Form PTO-1449 and/or  the additional information identified below in paragraph 3.

A copy of each required reference listed on the Form PTO-1449 is enclosed.

The references listed on the Form PTO-1449 were previously cited in application serial no. \_\_\_\_\_ to which the captioned application is seeking priority under 35 U.S.C. §120; therefore, a copy of each reference is not enclosed.

The present application is a U.S. national patent application filed after June 30, 2003, or an international application that entered the national stage under 35 USC § 371 after June 30, 2003. Therefore, copies of the cited U.S. patents and/or U.S. patent application publications are not enclosed.

1. This Information Disclosure Statement is submitted:

a.  within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);

- within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;
- before the mailing date of a first Office Action on the merits; or
- before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

b.  after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus:  the certification of paragraph 2 below is provided, or  a fee of \$180.00 is enclosed.

c.  after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

- that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in § 1.56(c) more than thirty (30) days prior to the filing of the information disclosure statement;
- that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of the Statement; or
- that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three (3) months prior to the filing of the Statement.

3.  Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

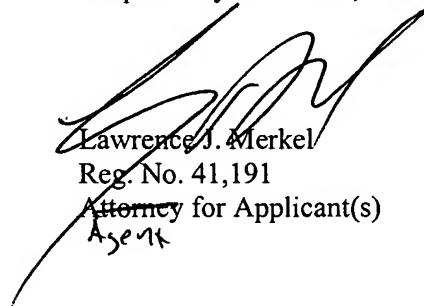
4. For each non-English language reference listed on the attached Form PTO-1449:

- reference is made to an English language translation submitted herewith, and/or
- reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or
- reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
- reference is made to the concise explanation contained in the specification of the present application at page(s) \_\_\_\_\_, and/or
- reference is made to the concise explanation set forth below:

5.  Applicant also offers the following comments for the Examiner's consideration:
6.  Also enclosed is a copy of a foreign search report citing these references.
7.  The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8.  Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzl, P.C. Deposit Account No. 501505/5681-67300/LJM.

Respectfully submitted,



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Date: 6/1/04

<p><b>Form PTO-1449</b> (modified)          List of Patents and Publications          For Applicant's Information          Disclosure Statement          (Use several sheets if necessary)</p> <p style="text-align: right;">JUN 03 2004 U.S. PATENT &amp; TRADEMARK OFFICE APPLICANT: Robert E. Cypher FILING DATE: October 30, 2003</p>					<p>SERIAL NO. 10/696,891          GROUP: 2131</p>			
<b>U.S. PATENT DOCUMENTS</b>								
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	B1	5,867,642	2/2/99	Vivio, et al.				
	B2	5,757,823	5/26/98	Chen, et al.				
<b>FOREIGN PATENT DOCUMENTS</b>								
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
	B3	IBM Technical Disclosure Bulletin, "Memory Chip Failure-Detection Mechanism", March 1991, Vol. 33, No. 10A, XP000110029, pp. 240 - 242.						
	B4	Saxena, et al., "Error Detection and Handling in a Superscalar, Speculative Out-of-Order Execution Processor System," IEEE, 1995, pp. 464-471.						
	B5	Rolf Johansson, "Two Error-Detecting and Correcting Circuits for Space Applications," IEEE 1996, pp. 436-439.						
	B6	IBM Technical Disclosure Bulletin, "Concurrent On-Lin Array Chip Sparing From Scrub-Assisted Fault Data," December 1991, Vol. 34, No. 7A, pp. 16-19.						

**EXAMINER:**

**DATE CONSIDERED:**

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.